

09/980891

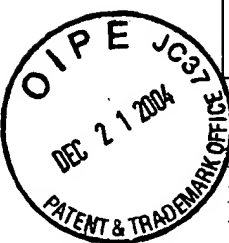
CgC

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: MS Post Issue, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: December 16, 2004 Signature: Tawana L. Garcia

(Tawana L. Garcia)

Docket No.: 02008/071001  
(PATENT)



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:  
Masahiro Ishida et al.

Patent No.: 6,801,049 B2

Issued: October 5, 2004

For: METHOD AND APPARATUS FOR DEFECT  
ANALYSIS OF SEMICONDUCTOR  
INTEGRATED CIRCUIT

Certificate  
JAN 27 2005  
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION  
PURSUANT TO 37 CFR 1.322

MS Post Issue  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted typographical errors which should be corrected.

On the first page under "Other Publications" in the fourth reference "INCs", should read --ICs--.

In the Claims:

In Claim 1, column 36, line 64, please insert --a--, before "voltage".

In Claim 8, column 37, line 60, please remove the comma after "analysis."

The errors were not in the application as filed by applicant; accordingly no fee is required.


JAN 31 2005

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Applicant believes no fee is due with this request. However, if a fee is due, please charge our Deposit Account No. 50-0591, under Order No. 02008/071001 from which the undersigned is authorized to draw.

Dated: December 16, 2004

Respectfully submitted,

By 

Thomas K. Scherer  
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**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO. : 6,801,049 *B2*  
DATED : October 5, 2004  
INVENTOR(S) : Masahiro Ishida et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the first page under "Other Publications" in the fourth reference "INCs", should read -- ICs--.

In Claim 1, column 36, line 64, please insert --a--, before "voltage".

In Claim 8, column 37, line 60, please remove the comma after "analysis".

MAILING ADDRESS OF SENDER:  
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PATENT NO. 6,801,049

No. of additional copies

Application No. (if known): 09/980,891

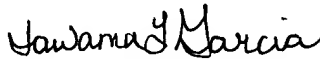
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Signature

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Tawana L. Garcia

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Request for Certificate of Correction (No Fee) (2 pages)